

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	981	(trim or trimming) same (clock)	US-PGPUB; USPAT	OR	ON	2005/02/09 10:36
L2	366	(trim or trimming) same (clock) same frequency	US-PGPUB; USPAT	OR	ON	2005/02/09 10:36



US 20040201939A1

(19) **United States**

(12) **Patent Application Publication**

**Shipton et al.**

(10) Pub. No.: **US 2004/0201939 A1**

(43) Pub. Date: **Oct. 14, 2004**

(54) **CLOCK TRIM MECHANISM FOR  
ONBOARD SYSTEM CLOCK**

**Publication Classification**

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Robert Walmsley, Balmain (AU)**

(51) Int. Cl.<sup>7</sup> ..... **H02H 5/04**

(52) U.S. Cl. .... **361/104**

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(57) **ABSTRACT**

(73) Assignee: **SILVERBROOK RESEARCH PTY  
LTD**

(21) Appl. No.: **10/727,210**

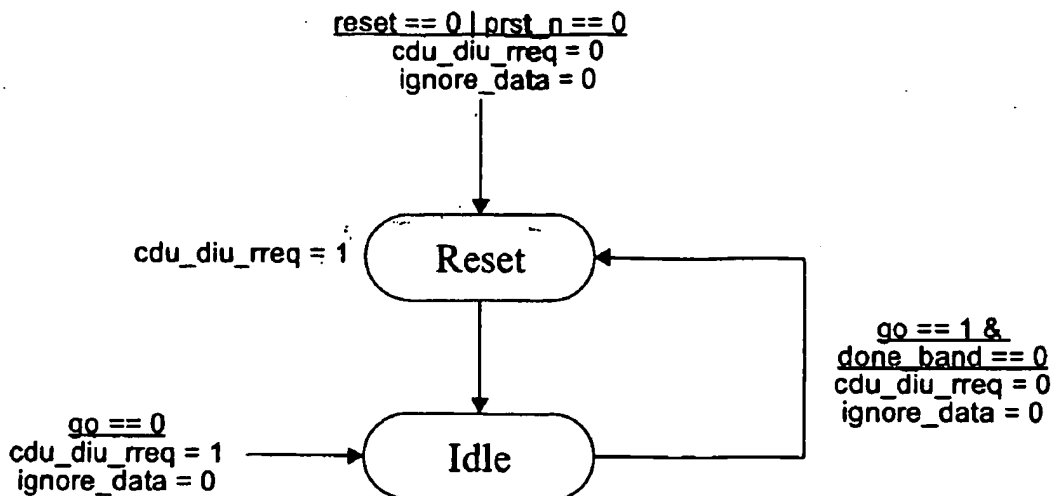
(22) Filed: **Dec. 2, 2003**

(30) **Foreign Application Priority Data**

Dec. 2, 2002 (AU) ..... **2002953134**

Dec. 2, 2002 (AU) ..... **2002953135**

An integrated circuit, comprising a processor, an onboard system clock for generating a clock signal, and clock trim circuitry, the integrated circuit being configured to: (a) receive an external signal; (b) determine either the number of cycles of the clock signal during a predetermined number of cycles of the external signal, or the number of cycles of the external signal during a predetermined number of cycles of the clock signal; (c) store a trim value in the integrated circuit, the trim value having been determined on the basis of the determined number of cycles; and (d) use the trim value to control the internal clock frequency.



Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
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Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10727210 on February 09, 2005

Original Classifications

2 358/1.9  
2 365/185.22

Cross-Reference Classifications

2 365/185.18  
2 714/748

Combined Classifications

2 330/10  
2 358/1.9  
2 365/185.18  
2 365/185.22  
2 710/36  
2 714/748

Titles of Most Frequently Occurring Classifications of Patents Returned  
From A Search of 10727210 on February 09, 2005

- 2 330/10 (1 OR, 1 XR)  
Class 330 : AMPLIFIERS  
330/10 MODULATOR-DEMODULATOR-TYPE AMPLIFIER
- 2 358/1.9 (2 OR, 0 XR)  
Class 358 : FACSIMILE AND STATIC PRESENTATION PROCESSING  
358/1.1 STATIC PRESENTATION PROCESSING (E.G.,  
PROCESSING DATA FOR PRINTER, ETC.)  
358/1.9 .Attribute control
- 2 365/185.18 (0 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/185.01 FLOATING GATE  
365/185.18 .Particular biasing
- 2 365/185.22 (2 OR, 0 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/185.01 FLOATING GATE  
365/185.18 .Particular biasing  
365/185.2 ..Reference signal (e.g., dummy cell)  
365/185.22 ...Verify signal
- 2 710/36 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/1 INPUT/OUTPUT DATA PROCESSING  
710/36 .Input/output access regulation
- 2 714/748 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/746 .Digital data error correction  
714/748 ..Request for retransmission

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PLUS Search Results for S/N 10727210, Searched February 09, 2005

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